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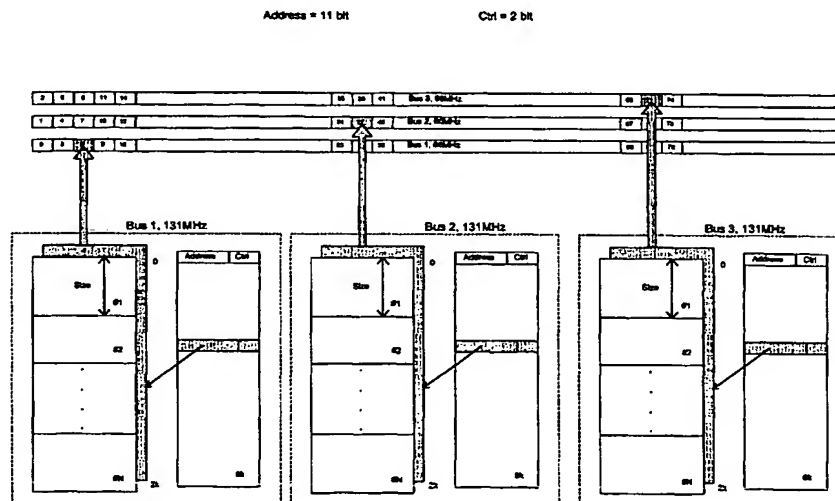
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- (71) Applicant (for all designated States except US): **TELEFONAKTIEBOLAGET LM ERICSSON [SE/SE]**; S-126 25 Stockholm (SE).
- (72) Inventors; and
- (73) Inventors/Applicants (for US only): **WEGO, Arild** [NO/NO]; Måkeveien 4, N-3400 Lier (NO). **HELLUM, Pål, Longva** [NO/NO]; Engertunet 3, N-1365 Blommenholm (NO).
- (74) Agent: **OSLO PATENTKONTOR AS**; Postboks 7007 M, N-0306 Oslo (NO).
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(54) Title: SWITCHING ARRANGEMENT INCLUDING TIME-SLOT BUSES AND SEVERAL BUFFERS



(57) **Abstract:** An arrangement for transmitting independent serial data streams through synchronous Time Division Multiplexing (TDM) switches with number of input and output lines is disclosed. At the receiving side of a switch, there is one data buffer per TDM bus buffering the data before transfer on the bus. A connection table associated with each buffer includes entries addressing the bytes in the buffer. The order of the addresses determines the order of the bytes as they are transferred over the bus. Also, at the transmitting side, there is one data buffer per bus, but only one common connection table. The connection table is divided into one memory area per output line, and determines the order of the data bytes, as they will occur at the respective output lines.